



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of  
Invention

CIRCUIT FOR AUTO-CLAMPING INPUT PINS TO A  
DEFINITE VOLTAGE DURING POWER-UP OR RESET

Application Number: 10/749342



Confirmation Number: 8208

First Named Applicant: Philip Ng

Attorney Docket Number: ATM-299

Art Unit: 2816

Search string: ( 2017813 or 5159206 or 5500611 or 6160429  
or 6335648 or 6509768 or 6650155 or  
20030214337 ).pn.

### US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	2017813	1991-05-21	Galbraith et al.	A1	307	475
<input type="checkbox"/>	2	5159206	1992-10-27	Tsay et al.	A1	307	272.3
<input type="checkbox"/>	3	5500611	1996-03-19	Popat et al.	A1	326	87
<input type="checkbox"/>	4	6160429	2000-12-12	Morrill	A1	327	143
<input type="checkbox"/>	5	6335648	2002-01-01	Matsuhita	A1	327	198
<input type="checkbox"/>	6	6509768	2003-01-21	Polizzi et al.	A1	327	143
<input checked="" type="checkbox"/>	7	6650155	2003-11-18	Nguyen et al.	A1	327	143

### US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	20030214337	2003-11-20	Miyagi	A1	327	215

Signature

 Examiner Name WELLS Date 6/10/05